

00699399-103100

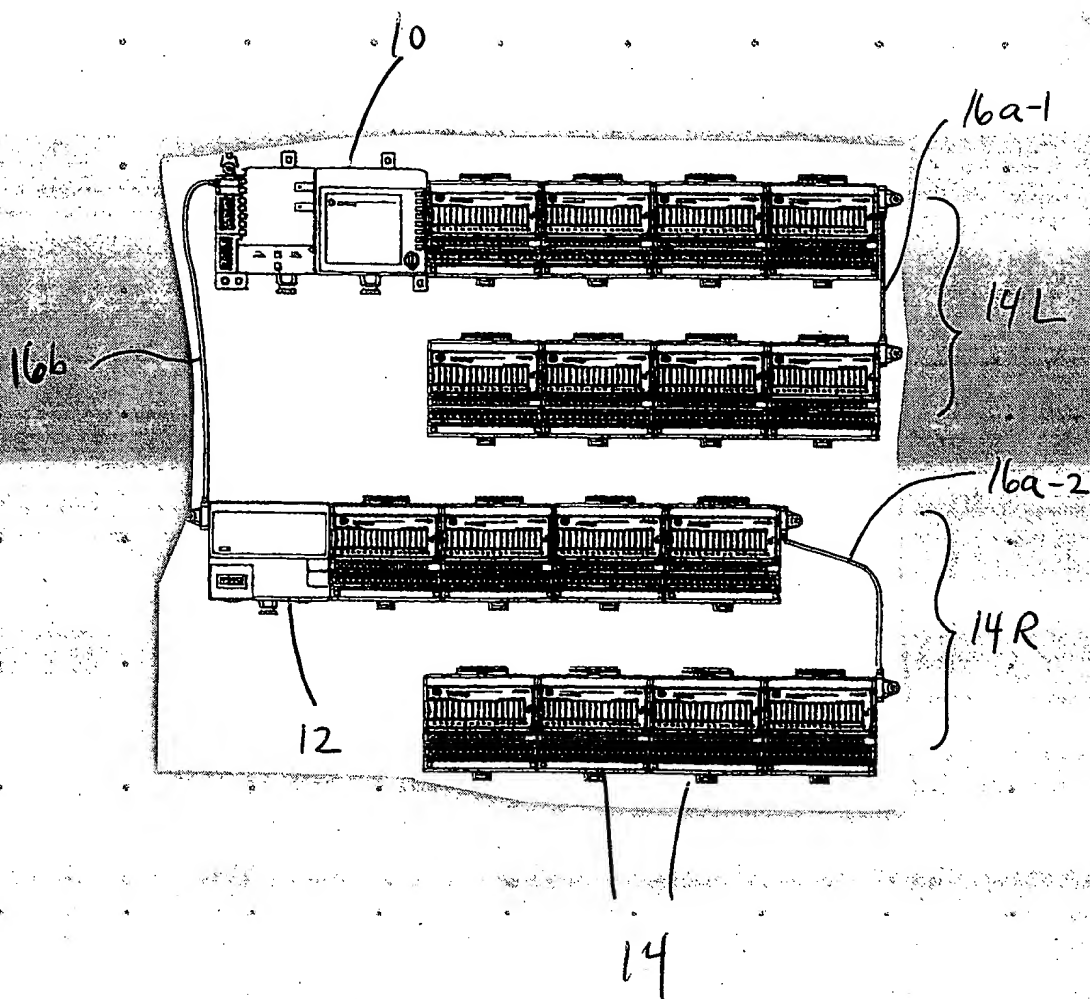


Fig. 1

The diagram illustrates a system 200 with the following components and connections:

- Interlock circuit (processor) 200**: Receives **OTHER SIGNALS** and **RESET (to load rail)**. It outputs **212** and **216a** to the **processing logic**.
- processing logic 202**: Receives **212** and **216a**. It outputs **218** to the **current detector** and **224** to the **data transfer control**. It also receives **fault** feedback.
- current detector 218**: Receives **218** and outputs **224** to the **processing logic**.
- data transfer control 208**: Receives **224** and **228** (from the sleeper). It outputs **230** to the **processing logic**.
- sleeper 240**: Receives **CLKLOW** and **222** (from the data transfer control). It outputs **228** to the **data transfer control**.
- 210**: A component (possibly a switch or sensor) connected to the **data transfer control** and the **processing logic**.
- 220**: A signal line connecting the **processing logic** to the **sleeper**.
- 222**: A signal line connecting the **data transfer control** to the **sleeper**.
- 228**: A signal line connecting the **sleeper** to the **data transfer control**.
- 230**: A signal line connecting the **data transfer control** to the **processing logic**.
- 232**: A signal line connecting the **processing logic** to the **data transfer control**.
- 234**: A signal line connecting the **processing logic** to the **data transfer control**.
- 236**: A signal line connecting the **processing logic** to the **data transfer control**.
- 238**: A signal line connecting the **processing logic** to the **data transfer control**.
- 240**: A signal line connecting the **sleeper** to the **data transfer control**.
- 242**: A signal line connecting the **sleeper** to the **data transfer control**.
- 244**: A signal line connecting the **sleeper** to the **data transfer control**.
- 246**: A signal line connecting the **sleeper** to the **data transfer control**.
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- 280**: A signal line connecting the **sleeper** to the **data transfer control**.
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- 288**: A signal line connecting the **sleeper** to the **data transfer control**.
- 290**: A signal line connecting the **sleeper** to the **data transfer control**.
- 292**: A signal line connecting the **sleeper** to the **data transfer control**.
- 294**: A signal line connecting the **sleeper** to the **data transfer control**.
- 296**: A signal line connecting the **sleeper** to the **data transfer control**.
- 298**: A signal line connecting the **sleeper** to the **data transfer control**.
- 300**: A signal line connecting the **sleeper** to the **data transfer control**.

Fig. 2

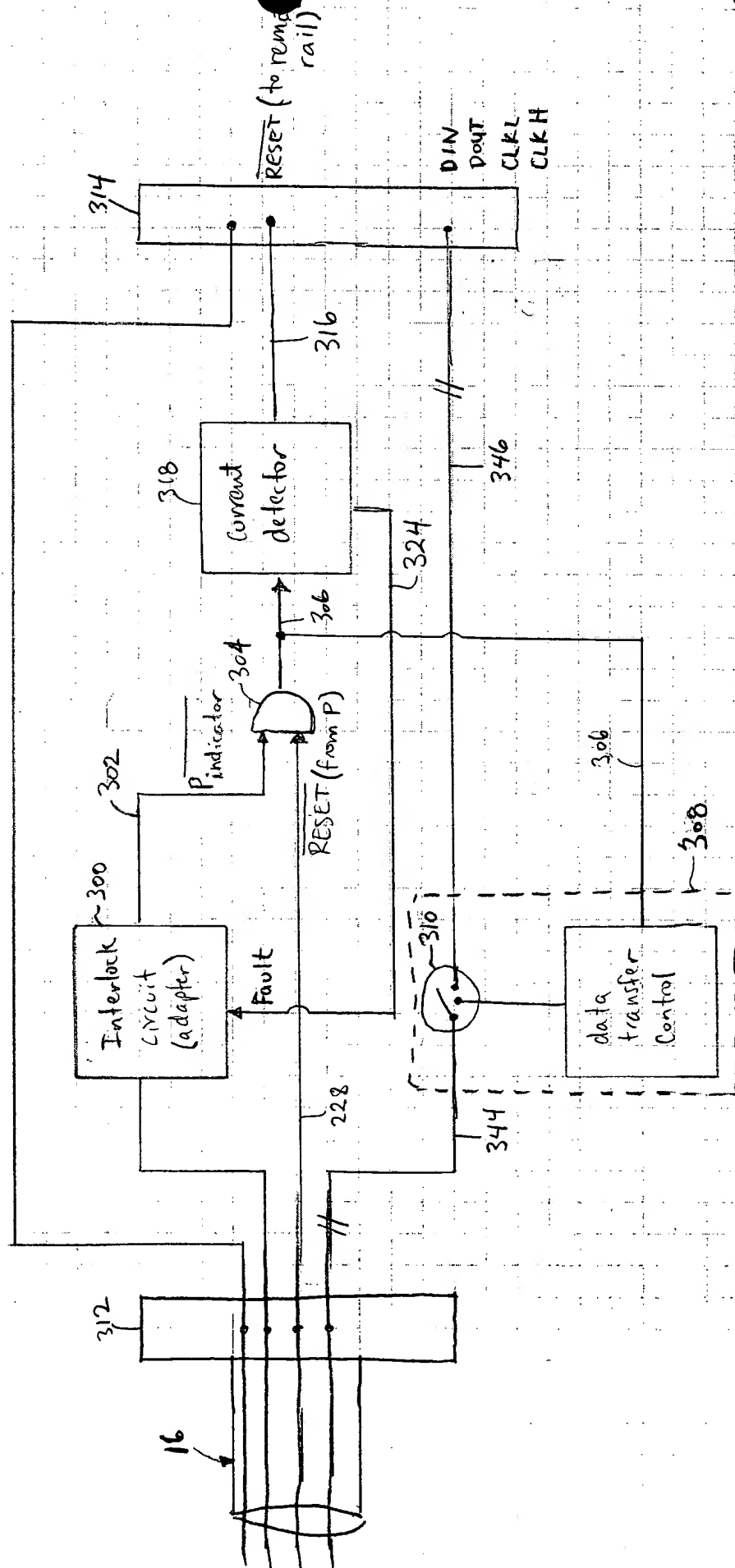
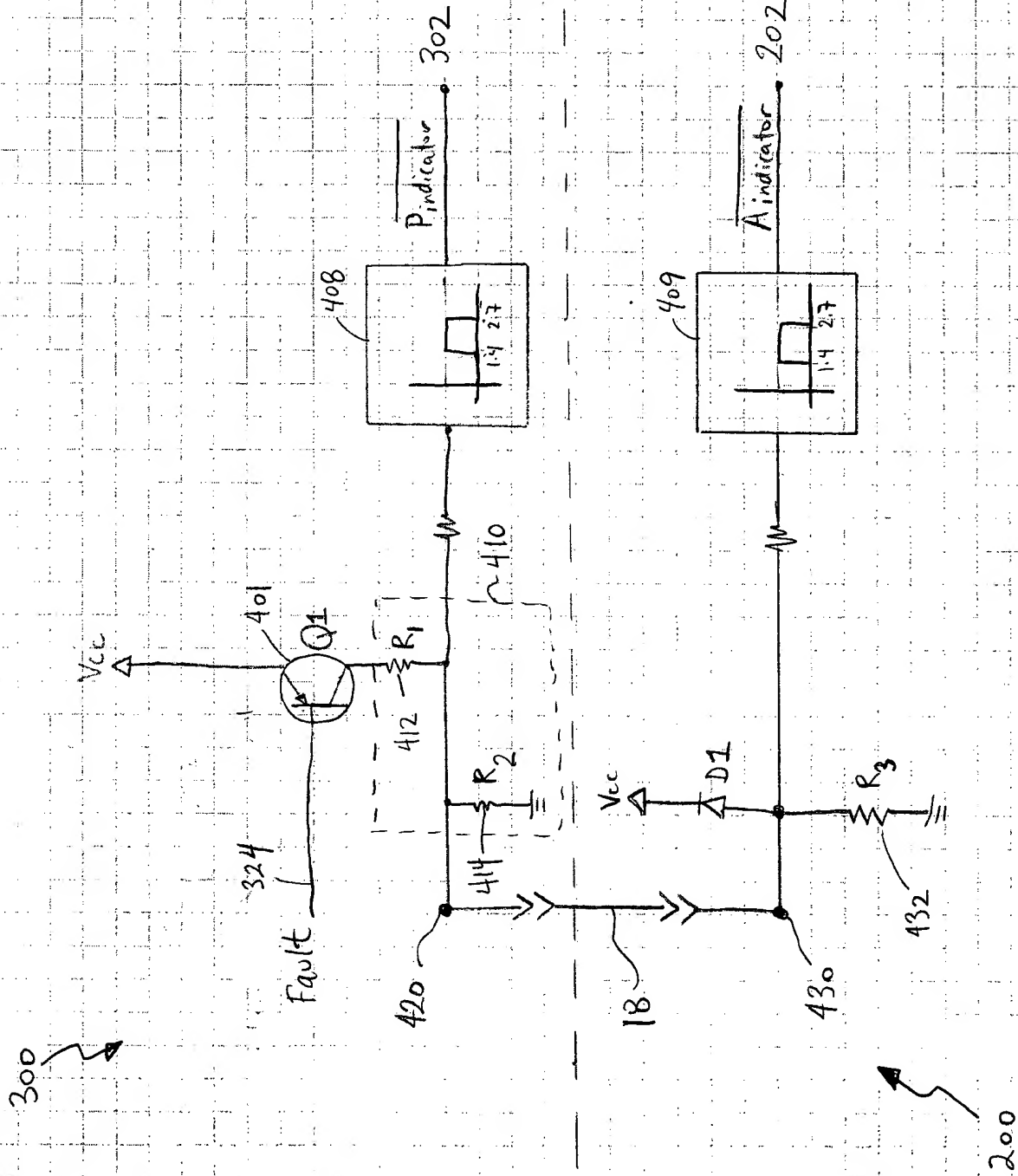


Fig. 3

Fig. 4



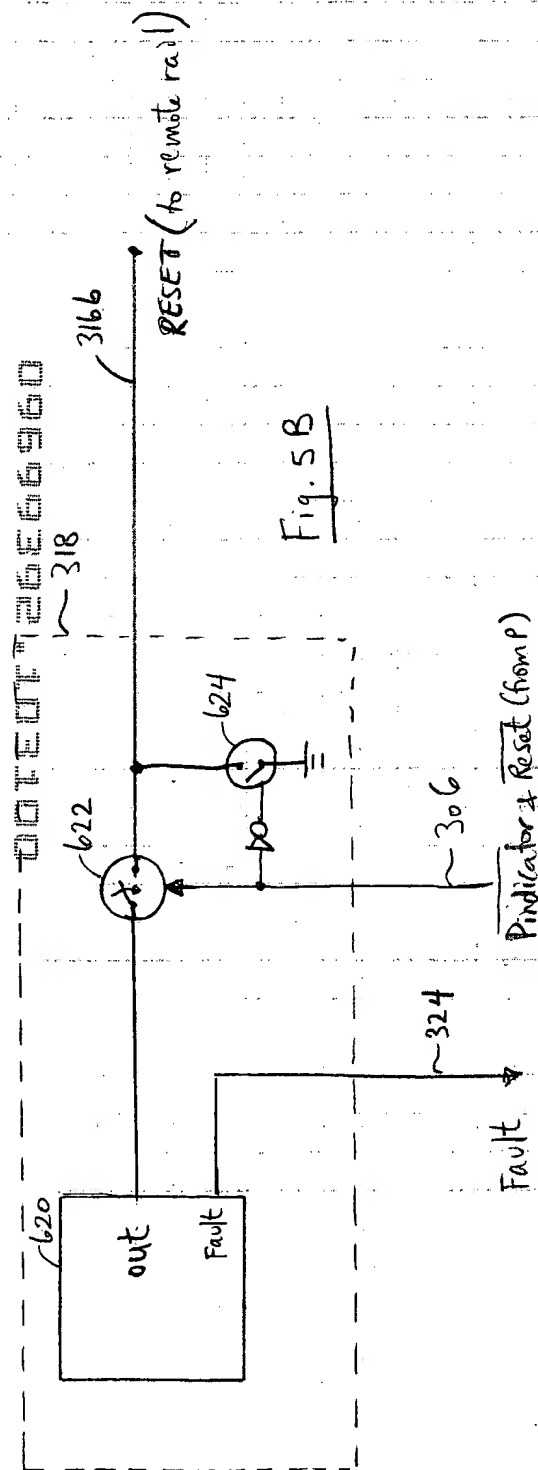


Fig. 5B

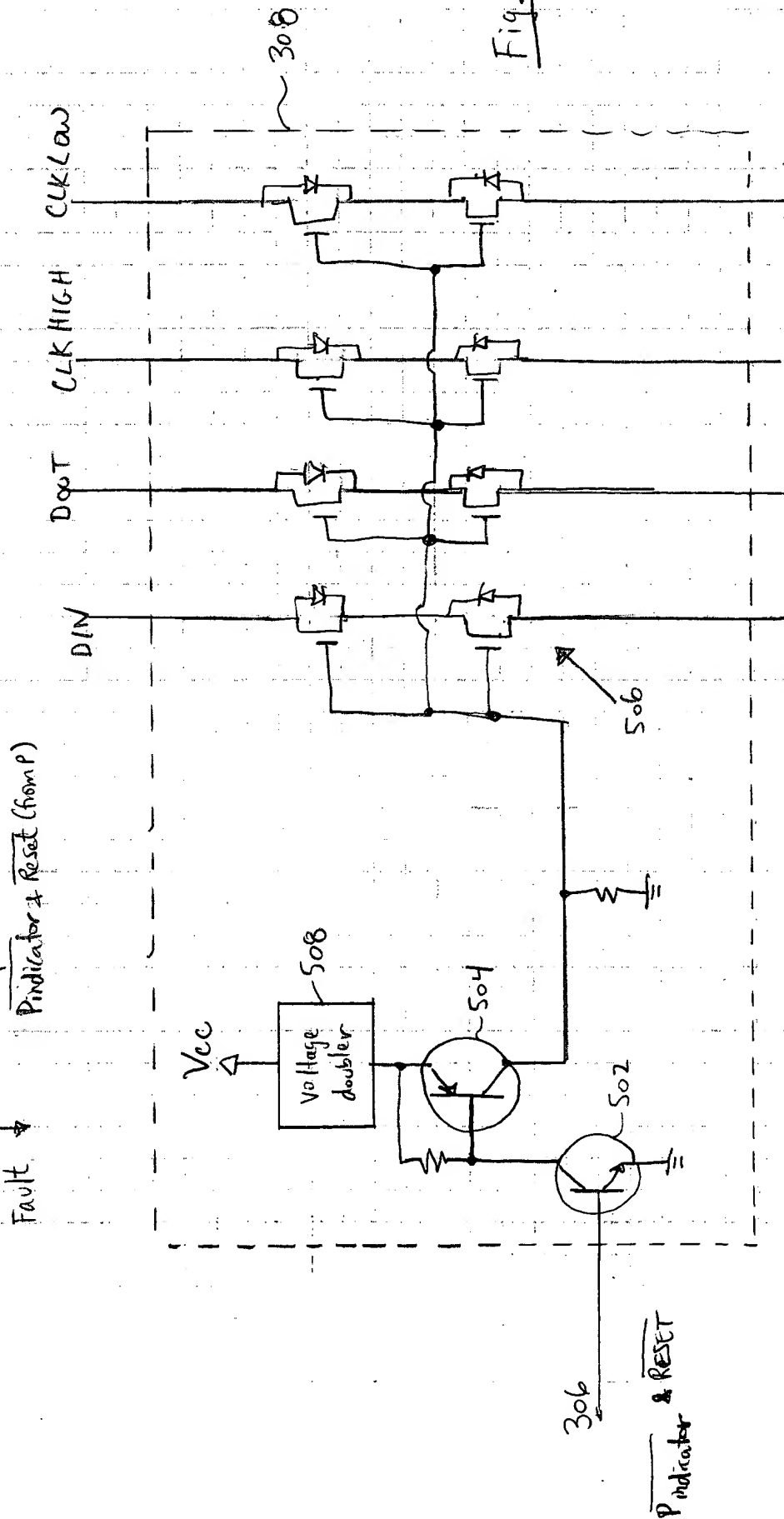
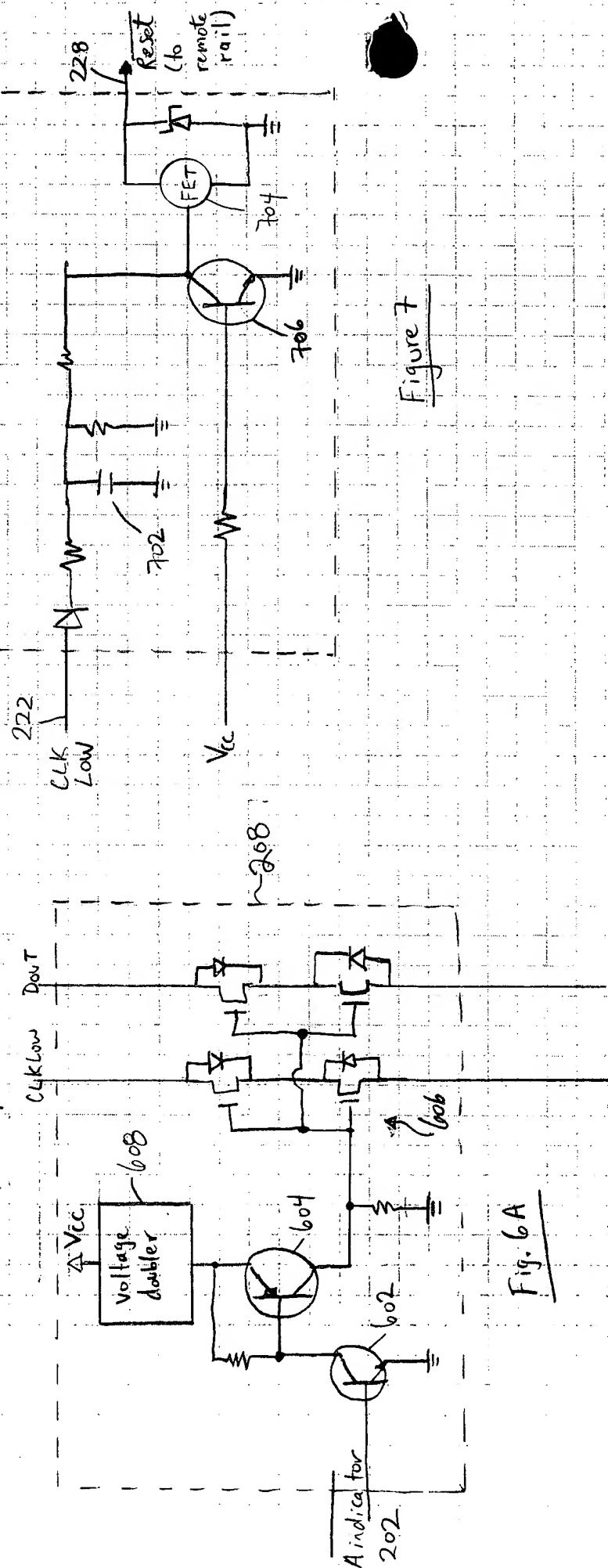
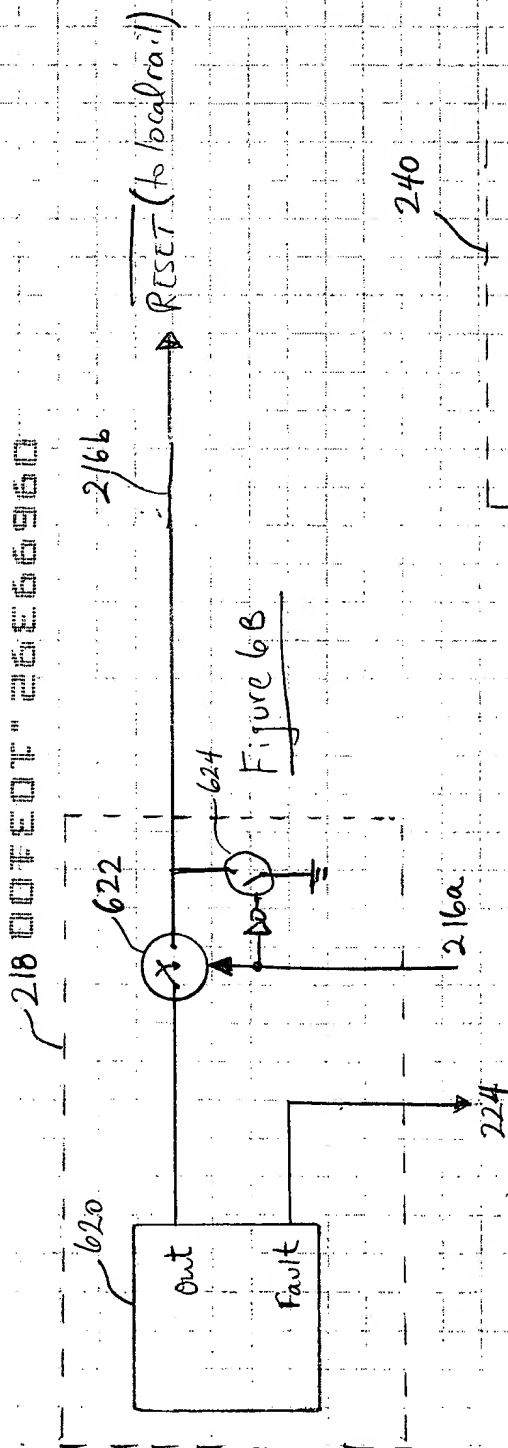


Fig. 5A

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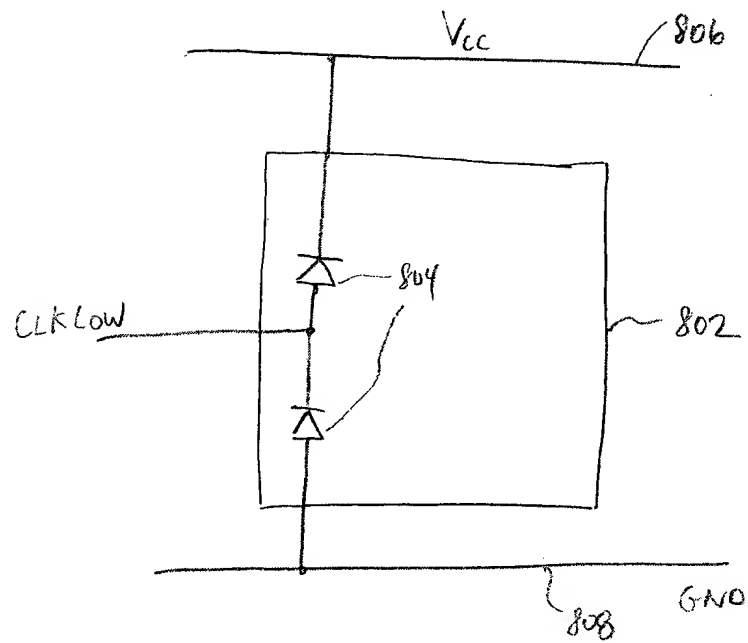


Fig. 8

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